

LTC6812 PCN Datasheet Changes

LTC6812-1

	MIN (OLD)	MIN (NEW)	SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
ADC Timing Specifications									
t_{CYCLE}	1692	1455	t_{CYCLE} (Figure 3, Figure 4, Figure 6)	Measurement + Calibration Cycle Time When Starting from the REFUP State in Normal Mode	Measure 15 Cells	● 1692	1956	2077	μs
	352	303			Measure 3 Cells	● 352	407	432	μs
	2382	2049			Measure 15 Cells and 2 GPIO Inputs	● 2382	2753	2924	μs
	145.2	124.9		Measurement + Calibration Cycle Time When Starting from the REFUP State in Filtered Mode	Measure 15 Cells	● 145.2	167.8	178.2	ms
	29.1	25.0			Measure 3 Cells	● 29.1	33.6	35.7	ms
	203.2	174.8			Measure 15 Cells and 2 GPIO Inputs	● 203.2	234.9	249.5	ms
	811	697		Measurement + Calibration Cycle Time When Starting from the REFUP State in Fast Mode	Measure 15 Cells	● 811	937	996	μs
	176	151			Measure 3 Cells	● 176	203	215	μs
	1149	988			Measure 15 Cells and 2 GPIO Inputs	● 1149	1328	1410	μs
t_{SKEW1}	168	144	t_{SKEW1} (Figure 6)	Skew Time. The Time Difference Between GPIO2 and Cell 1 Measurements, Command = ADCVAX	Fast Mode	● 168	194	206	μs
	470	404			Normal Mode	● 470	543	577	μs
t_{SKEW2}	162	139	t_{SKEW2} (Figure 3)	Skew Time. The Time Difference Between Cell 15 and Cell 1 Measurements, Command = ADCV	Fast Mode	● 162	187	198	μs
	464	400			Normal Mode	● 464	536	569	μs
t_{SKEW3}	127	109	t_{SKEW3} (Figure 6)	Skew Time. The Time Difference Between Cell 15 and GPIO1 Measurements, Command = ADCVAX	Fast Mode	● 127	147	156	μs
	354	304			Normal Mode	● 354	409	434	μs

RELIABILITY DATA LTC6812 & LTC6813 PCN

7/12/2019

• **Operating Life Test**

PACKAGE TYPE	SAMPLE SIZE	OLDEST DATE CODE	NEWEST DATE CODE	K DEVICE HOURS AT +125°C	NUMBER OF FAILURES
LQFP	154 154	1916	1916	25.87 25.87	0 0

• **Early Life Failure Rate Test**

PACKAGE TYPE	SAMPLE SIZE	OLDEST DATE CODE	NEWEST DATE CODE	K DEVICE HOURS AT +125°C	NUMBER OF FAILURES
LQFP	800 800	1916	1916	38.40 38.40	0 0

Form: 00-03-6209B.

Rev 1

1. Electro-Static Discharge (ESD) Test Results

1.1 Test Description

The HBM ESD testing was performed on a THERMOFISHER Mk.2 using the Human Body Module per JESD22-A114. This test is performed for classification only. Class 1A > ± 250 V, Class 1B > ± 500 V, Class 1C > ± 1000 V, Class 2 > ± 2000 V, Class 3A > ± 4000 V and Class 3B > ± 8000 V. A copy of the circuit is shown below:

1.2 Test Circuit & Condition

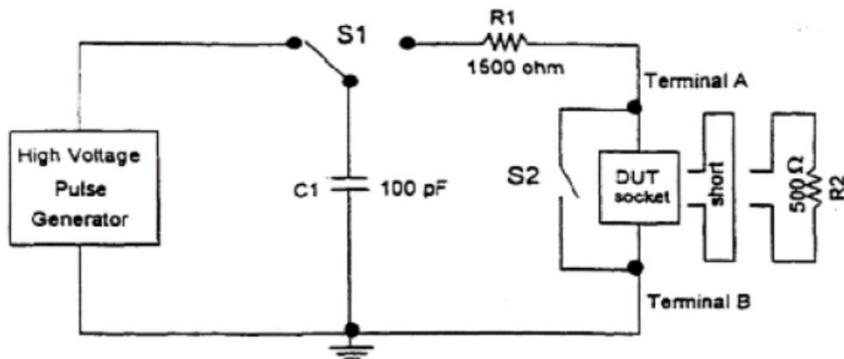


Figure 1 — Typical equivalent HBM ESD circuit

NOTE 1 The performance of any simulator is influenced by its parasitic capacitance and inductance.

NOTE 2 Precautions must be taken in tester design to avoid recharge transients and multiple pulses.

NOTE 3 R2, used for initial equipment qualification and requalification as specified in 3.1, shall be a low inductance, 4000 V, 500 Ω resistor with +/-1% tolerance.

NOTE 4 Stacking of DUT socket adaptors (piggybacking) is allowed only if the waveforms can be verified to meet the specifications in Table 1.

NOTE 5 Reversal of terminals A and B to achieve dual polarity is not permitted.

NOTE 6 S2 shall be closed at least 10 milliseconds after the pulse delivery period to ensure the DUT socket is not left in a charged state.

NOTE 7 R1, 1500 Ω +/- 1%.

NOTE 8 C1, 100 pF +/- 10% (effective capacitance).

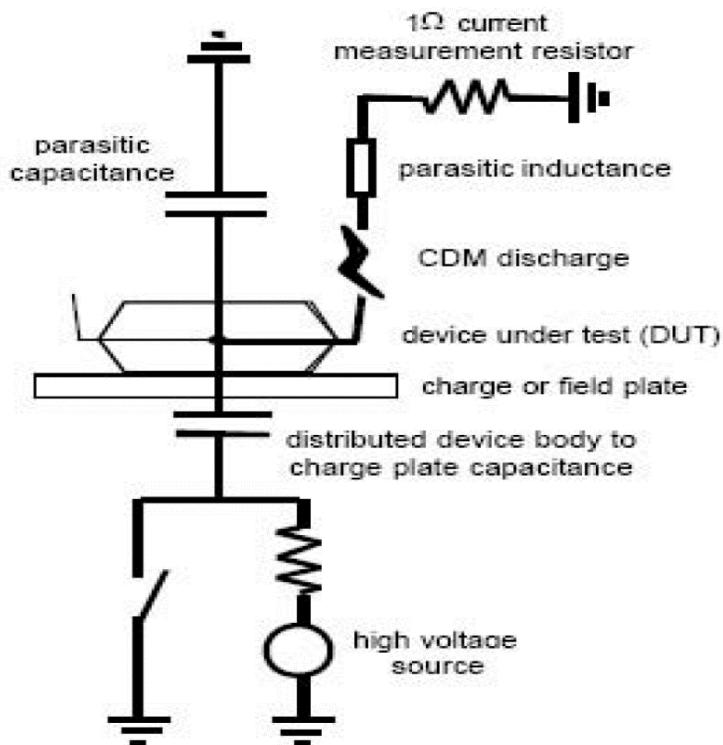
1.3 ESD Data

Device	Model	S/S	Pins	Voltage Passed	Voltage Failed
LTC6813	HBM Class 1C	3	All Pins	> ± 1000 V	

1.7 Test Description

The Charged Device Model (CDM) ESD testing was performed on a THERMOFISHER RCDM system per ESDA ESD STM5.3.1-1999 / AEC-Q100-011-Rev-B. This test is performed for information only. A copy of the circuit is shown below:

1.8 Test Circuit & Condition



(b) Field induced charge CDM

1.9 ESD Data

Device	Model	S/S	Pins	Voltage Passed	Voltage Failed
LTC6813	CDM	3	All Pins	> \pm 750V	

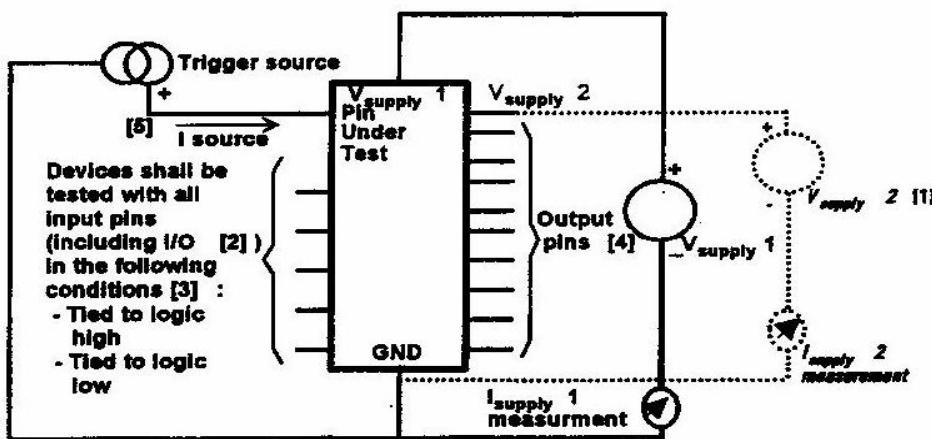
4. Latch-Up Test Results

4.1 Test Description

Latchup Testing was performed at +25°C and +125°C using the LTX Integrated Circuit Test system. The Power Supply pins are biased to the appropriate Datasheet specifications and the individual non-Power Supply pins are tested incrementally while the current is monitored until failure occurs.

4.2 Test Circuit & Condition

4.2.1 Test Circuit 1

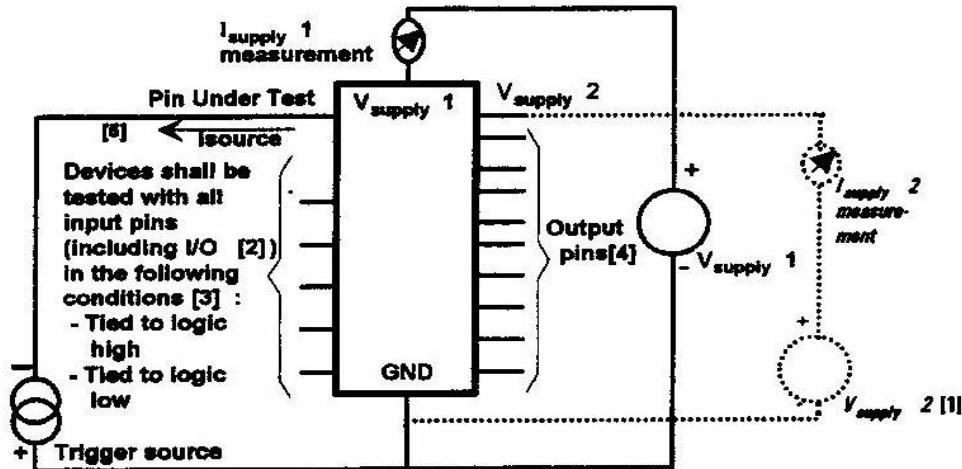


1. DUT biasing shall include additional $V_{supplies}$ as required.
2. DUT shall be preconditioned so that all I/O pins are placed in a valid state per 4.1. I/O pins in the output state shall be open circuit.
3. Logic high and logic low shall be per the device specification. When logic levels are used in respect to a non-digital device, it means the maximum high or minimum low voltage that can be supplied to the pin per the device specifications, unless these conditions violate device setup condition requirements.
4. Output pins shall be open circuit except when latch-up tested.
5. The trigger test condition is defined in figure 2 and table 1.

NOTE: Dynamic devices may have timing signals applied per 4.2.3.

Figure 5 - The equivalent circuit for positive input/output I-test latch-up testing

4.2.2 Test Circuit 2



1. DUT biasing shall include additional Vsupplies as required.
2. DUT shall be preconditioned so that all I/O pins are placed in a valid state per 4.1. I/O pins in the output state shall be open circuit.
3. Logic high and logic low shall be per the device specification. When logic levels are used with respect to a non-digital device, it means the maximum high or minimum low voltage that can be supplied to the pin per the device specification, unless these conditions violate the device setup condition requirements.
4. Output pins shall be open circuit except when latch-up tested.
5. The trigger test condition is defined in figure 3 and table 1.

NOTE: Dynamic devices may have timing signals applied per 4.2.3.

Figure 6 - The equivalent circuit for negative input/output I-test latch-up testing

4.3 Latch-Up Data

Device	Mode	Current	Temp	S/S	Results
LTC6813	CKT1 & CKT2	>±200mA	+25°C	5	PASS
	CKT1 & CKT2	>±100mA	+125°C	5	PASS

DeltaQualifikationsMatrix

Allgemeines

Kurze Produkt- und Technologiezyklen elektronischer Bauelemente sowie neue Umweltauflagen (Bleiverbot, Flammhemmer, ...) führen häufig zu prozeß- und werkstofftechnischen Änderungen an Bauelementen, Leiterplatten, Verbindungstechnik und Schaltung, welche evaluiert werden müssen. Eine geeignete Methodik zur Handhabung von Änderungen an elektronischen Bauelementen beschreibt die ZVEI "Guideline for Customer Notifications of Product and/or Process Changes (PCN) of Electronic Components specified for Automotive Applications". Ein wesentlicher Teil dieser Guideline sind die hier vorliegenden Matrizen, welche sich als Empfehlungen für die Evaluierung von typischen Änderungen an elektronischen Bauelementen verstehen. Dies sollte Teil des offenen und risikobewussten Dialoges zwischen Lieferant und Kunden sein.

Diese DeltaQualifikationsMatrizen wurden durch den Industriearbeitskreis "PCN DeltaQualifikationsMatrix" und den Bautelexperten des ZVEI Arbeitskreises "PCN-Methodik" erarbeitet. Der Inhalt wurde basierend auf dem aktuellen Stand der Technik erstellt und erhebt keinen Anspruch auf Vollständigkeit. Im Einzelfall ist ggf. ein abweichendes Vorgehen abzustimmen, da kundenspezifische Vereinbarungen zur Qualifikation zu berücksichtigen sind.

Anwendung der DeltaQualifikationsMatrix (auszufüllen durch den Bauelementhersteller)

- a) Diese Tabelle ist nur bei Änderungen anzuwenden. Neuqualifikationen und Sonderqualifikationen (z.B. Verguß von Modulen) sowie Information Notes bleiben von diesen Matrizen unberührt.
- b) Ist eine Änderung in dieser Tabelle nicht aufgeführt, so ist der Qualifikationsumfang zwischen Kunde und Lieferant abzustimmen.
- c) Die Matrix der Aktiven Bauelemente ist so aufgebaut, dass zwischen integrierten Halbleitern (AEC-Q101 Rev.H) und diskreten Halbleitern (AEC-Q101 Rev. D1) auszuwählen ist (Zelle D4). Für Passive Bauelemente gilt die AEC-Q200. Für LEDs gilt die IEC 60810.
- d) Alle Änderungen in der PCN sind in der Spalte B durch ein Kreuz (x) zu markieren und werden dadurch farblich hervorgehoben. Sofern dies geschehen ist, werden im Feld "Tests, which should be considered for the appropriate process change" (Zeile 83 für Aktive Bauelemente, Zeile 466 für Passive Bauelemente oder in Zeile 77 für LED's) alle in Betracht zu ziehenden Zuverlässigkeitstests angezeigt.
- e) In "Tests, which should be considered for the appropriate process change after selection of condition table" (Zeile 85 für Aktive Bauelemente, Zeile 468 für Passive Bauelemente oder Zeile 79 für LEDs) wird die Anpassung der in Betracht zu ziehenden Tests in Folge der Relevanz bezüglich der Änderung berücksichtigt.
- f) In "Suppliers performed tests" (Zeile 87 für Aktive Bauelemente, Zeile 470 für Passive Bauelemente oder Zeile 81 für LED's) dokumentiert der Bauelementhersteller die durchgeführten bzw. geplanten Tests.
- g) Falls von der Testempfehlung abweichen wird, so sollten diese Abweichungen vom Bauelementhersteller angezeigt und kommentiert werden. Hierzu ist der Bereich "Reason for exception of tests" (Zeile 89 für Aktive Bauelemente, Zeile 472 für Passive Bauelemente oder Zeile 83 für LED's) zu verwenden.
- Werden die in Betracht zu ziehenden Tests durch generische Daten (G) belegt, ist dies ebenfalls hier anzugeben und zu begründen.

Die Einstufung des Untersuchungslevel erfolgt in folgende Kategorien

- "C: Component level": Die Evaluierung der Änderung am Bauelement ist durch Untersuchungen ausschließlich am Bauelement beim Bauelementhersteller durchführbar. Zur Evaluierung der Änderung dürfen Ergebnisse aus bereits durchgeföhrten Untersuchungen herangezogen werden, wenn diese zu einem ähnlichen Bauelement bereits vorliegen (**Generische Daten**).
- "B: Board level": Die beschriebene Änderung hat möglicherweise Einfluss auf die Verarbeitbarkeit des Bauelementes im Steuergerät. Die Evaluierung der Änderung wird unter C beim Bauelementhersteller durchgeführt. Zusätzlich ist durch den Kunden/Steuergeräthersteller die Verarbeitbarkeit zu prüfen, die z.B. abhängt von der Änderung, Zuverlässigkeitstests auf applicationsrelevanten Testboards erforderlich.
- "A: Application level": Die beschriebene Änderung hat möglicherweise Einfluss auf die Applikation/ das Steuergerät. Die Evaluierung der Änderung wird unter C oder B durchgeführt. Zusätzlich ist vom Kunden/Steuergeräthersteller der Einfluss der Änderung im Steuergerät durch geeignete Untersuchungen zu bewerten. Dieses Vorgehen ist mit dem OEM abzustimmen. Hierbei ist zu berücksichtigen, ob die Steuergeräte- / Baugruppenanforderungen durch andere Qualifikationen bereits hinreichend abgesichert sind (**applikationsspezifische Risikobetrachtung**).

* *: Not relevant for qualification matrix": Änderung(en), die nicht in A, B oder C eingestuft werden können und somit nicht relevant für die DeQuMa sind.

Information Notes

Änderungen die nur eine Infomation Note benötigen (bei der Bewertung Risk on Supply Chain als "I" gekennzeichnet), dürfen nicht in der DeQuMa angekreuzt werden, da Sie ansonsten den erforderlichen Evaluierungslevel verfälschen. Für als "I" bewertete Änderungen ist das Information Note Formblatt zu verwenden.

Wichtige Hinweise

- Zur formgerechten Anwendung der DeltaQualifikationsMatrizen steht auf der Homepage des ZVEI AK ein Tutorial bereit (ZVEI-Tutorial).
- ID Nummer: ist eine eindeutige Identifikationsnummer für jede angegebene Änderung, die in den ZVEI PCN DeltaQualifikationsMatrizen identifiziert ist. Die gleiche ID Nummer wird zur Identifizierung der Änderung im PCN Form Sheet verwendet.
- Die mittels Matrix identifizierten Tests sind in **Betracht zu ziehen**, d.h. es ist zu prüfen, ob der jeweilige Test für die spezifische Änderung in dieser Form notwendig ist. Abweichungen oder generische Daten sind im Detail zu begründen.
- Die Spalte "Further applicable conditions", Bemerkungen und Fußnoten sind unbedingt zu beachten, da sie wichtige Hinweise und Einschränkungen enthalten.
- Zur Nutzung aller Funktionen muss in Excel die Anwendung von Makros freigegeben sein.

DeltaQualificationMatrix

General

Short product and technology cycles as well as new environmental regulations („Pb-free“, flame retardants, ...) frequently result in process and material changes of components, printed circuit boards, assembly techniques and circuit layout which have to be evaluated. The ZVEI "Guideline for Customer Notifications of Product and/or Process Changes (PCN) of Electronic Components specified for Automotive Applications" describes an appropriate methodology for dealing with changed electronic components. The qualification matrices in this guideline are recommendations for how to assess typical changes of electronic components. These recommendations promote an open risk-based discussion between supplier and customer regarding qualifications.

The DeltaQualificationMatrices were developed by the Industry Task Force Team "PCN-DeltaQualificationMatrix" together with component experts from the ZVEI Working Group "PCN-Methodology". Actual content represents state-of-the-art technology and does not claim to be comprehensive. Deviation from proposed guideline should be mutually agreed as customer specific requirements have to be considered.

DeltaQualificationMatrix Application (completion by component manufacturer)

- a) This table has to be used for changes only. The matrices are not applicable for new product, special qualifications (for instance for encapsulation of module) or Information Notes.
- b) If a change is not listed in this table, the qualification plan has to be defined and agreed between customer and supplier.
- c) The matrix for Active Components requires the user to choose between integrated circuits (AEC-Q101 Rev. H) and discrete semiconductors (AEC-Q101 Rev.D1) (cell D4). For Passive Components AEC-Q200 is used. For LED's the IEC 60810 is used.
- d) All changes as listed in the PCN have to be marked by a cross (x) in column B and will appear colored. The relevant reliability tests are then shown in "Tests, which should be considered for the appropriate process change" (row 83 for Active Components, row 466 for Passive Components, respectively in row 77 for LED's).
- e) In "Tests, which should be considered for the appropriate process change after selection of condition table" (see row 85 for Active Components, row 468 for Passive Components, or row 79 for LED's) is for modification of the found relevant tests under consideration of the weight of change. Related table "Conditions" has to be assessed per proposed letters with an (x).
- f) In "Suppliers performed tests" (here row 87 for Active Components, row 470 for Passive Components, or row 81 for LED's) the component manufacturer documents the planned and performed tests.
- g) In case of deviations from tests, which should be considered this should be notified and commented by the component manufacturer in the area "Reason for exception of tests" (see row 89 for Active Components, row 472 for Passive Components, or row 83 for LED's). Test results in form of generic data (G) are allowed when notified and justified.

Evaluation Levels are categorized as follows

- "C: Component level": The evaluation of a change at component level by the component manufacturer is sufficient. Generic data from other relevant evaluations can be used.
- "B: Board level": The intended change may influence the processability / manufacturability of the component at board level. Therefore additional evaluation by customer may be necessary, for example reliability tests on application relevant testboards, depending on change.
- "A: Application level": The intended change described in the PCN may influence the properties of the application (e.g. Electronic Control Unit). In addition to the evaluation under C or B the influence of the change in the application is evaluated by suitable investigations by the customer. The scope of the evaluation has to be aligned with the OEM. It has to be considered whether the application / assembly requirements are already sufficiently safeguarded by other qualifications (**application specific risk assessment**).

* *: Not relevant for qualification matrix": Changes which fulfill neither A,B nor C definitions

Information Notes

Changes indicated as "I" shall not be marked in the DeQuMa. For those changes the InformationNote sheet shall be used. As the DeQuMa is desired for PCN only, a marking of "I"-changes would automatically influence evaluation level and test effort.

Important Notes

- To use the matrices in the right form the ZVEI working group provides a Tutorial on its homepage (ZVEI-Tutorial)
- ID number: is a unique identification number for each indicated change defined in the ZVEI PCN DeltaQualificationMatrices. The same ID number is used in the PCN Form sheet to identify the change.
- Tests identified by the matrix have to be considered and checked if they are necessary to assess the specific change. Test modifications or generic data have to be justified in detail.
- "Further applicable conditions", comments and notes need attention, as they provide important hints and limitations.
- In order to use all functions in EXCEL, macros have to be allowed.

History of DeQuMa

Version	Remarks
2.0	Revised by ZVEI PCN Methodology Workgroup in March 2015
2.1	Released March 2015
2.1.1	Active Components - delete write protection in comments
2.2	Solved problems with some ActiveX configurations
2.2.2	Solved Problems in Active Components
2.2.3	Solved Problems ActiveX, Active Components SEM-DE-02 (Design changes in routing) error fixed
2.2.4	Minor fixes
3.0	General Revision by ZVEI PCN Methodology Workgroup in June 2016 Changes are indicated by underlining in the read only version named Changes_DeQuMa_rev3_vs_rev2.xlsx
3.0.4	Expert Release
3.0.5	Fixing of macro bugs
3.1	Final Release (orthographic and punctuation corrections)

Worked on: (Name, Function)	Sabine Koschack
Date:	18/07/2019
PCN number:	19_0141
Signature:	[Signature]
For integrated circuit or discrete semiconductor: [Signature]	AEC-Q100 Revision H -

Tests, which should be considered for the appropriate process char

Tests, which should be considered for the appropriate process change after selection of condition table

Suppliers performed tests (mark with an 'X' for done or 'G' for generic)

Reason for exception of tests and/or usage of generic data:

-	Not required
I	Information Notes required
P	PCN required
A letter or "X" indicates that performance of that stress test should be considered for the appropriate process change	
CONDITIONS	
A	Termination equipment only
C	Ceramic only
D	Dielectrically charged, discharged, charged required
E	Capacitive trimmers only
F	Film products only
N	Networks only
R	Resistor only
S	Surface components only
W	Wirewound products only
Y	Component not hermetically sealed
Note 1: For parts marked with ink only, Laser and stamp marked parts shall be exempt	
=> Please mark 'NO' with 'X', default = YES'	

Worked on: (Name, Function)	Max Mustermann
Date:	
PCN number:	
Signature:	

Basis: IEC 6081

<input checked="" type="checkbox"/> A No required <input type="checkbox"/> B Information flow needs <input type="checkbox"/> C Control flow needs	Value or "C" <p>The value or performance of that sheet test should be considered for the appropriate process change.</p>
<input checked="" type="checkbox"/> CONDITIONS <input type="checkbox"/> A Only if device placed applicable (Ag intended to fail for this test) <input type="checkbox"/> B Only if component placement is changed <input type="checkbox"/> C Only if component placement is changed <input type="checkbox"/> D Only if minimum values are changing <input type="checkbox"/> E Only if maximum values are changing <input type="checkbox"/> F Only if material properties are changed <input type="checkbox"/> G Only if new bond technology is used <input type="checkbox"/> H Only if new bond technology is used <input type="checkbox"/> I Only if leadframe/Substrate dimensions are changed <input type="checkbox"/> J Only if leadframe/Substrate dimensions are changed <input type="checkbox"/> K Only for glued chips <input type="checkbox"/> L Only if material properties are changed <input type="checkbox"/> M Only if new bond technology is used <input type="checkbox"/> N Only if new bond technology is used <input type="checkbox"/> O Only if Board Reliability is affected <input type="checkbox"/> P Only for non-hermetic device <input type="checkbox"/> Q Only for hermetic device <input type="checkbox"/> R Only for larger package <input type="checkbox"/> S Only for smaller package <input type="checkbox"/> T Only if sheet parameters are affected <input type="checkbox"/> U Only if sheet parameters are affected <input type="checkbox"/> V Only if sheet parameters are affected <input type="checkbox"/> W Only if sheet parameters are affected <input type="checkbox"/> X Only if sheet parameters are affected <input type="checkbox"/> Y Only if sheet parameters are affected <input type="checkbox"/> Z Only if sheet parameters are affected	